

Output of a 3-bit LFSR

Iteration #	LFSR Outputs ($X_2X_1X_0$)
1	011
2	001
3	100
4	010
5	101
6	110
7	111
8	011

Figure 1

3 Bit Linear Feedback Shift Register (with feedback taps at bit 0 and bit 1)

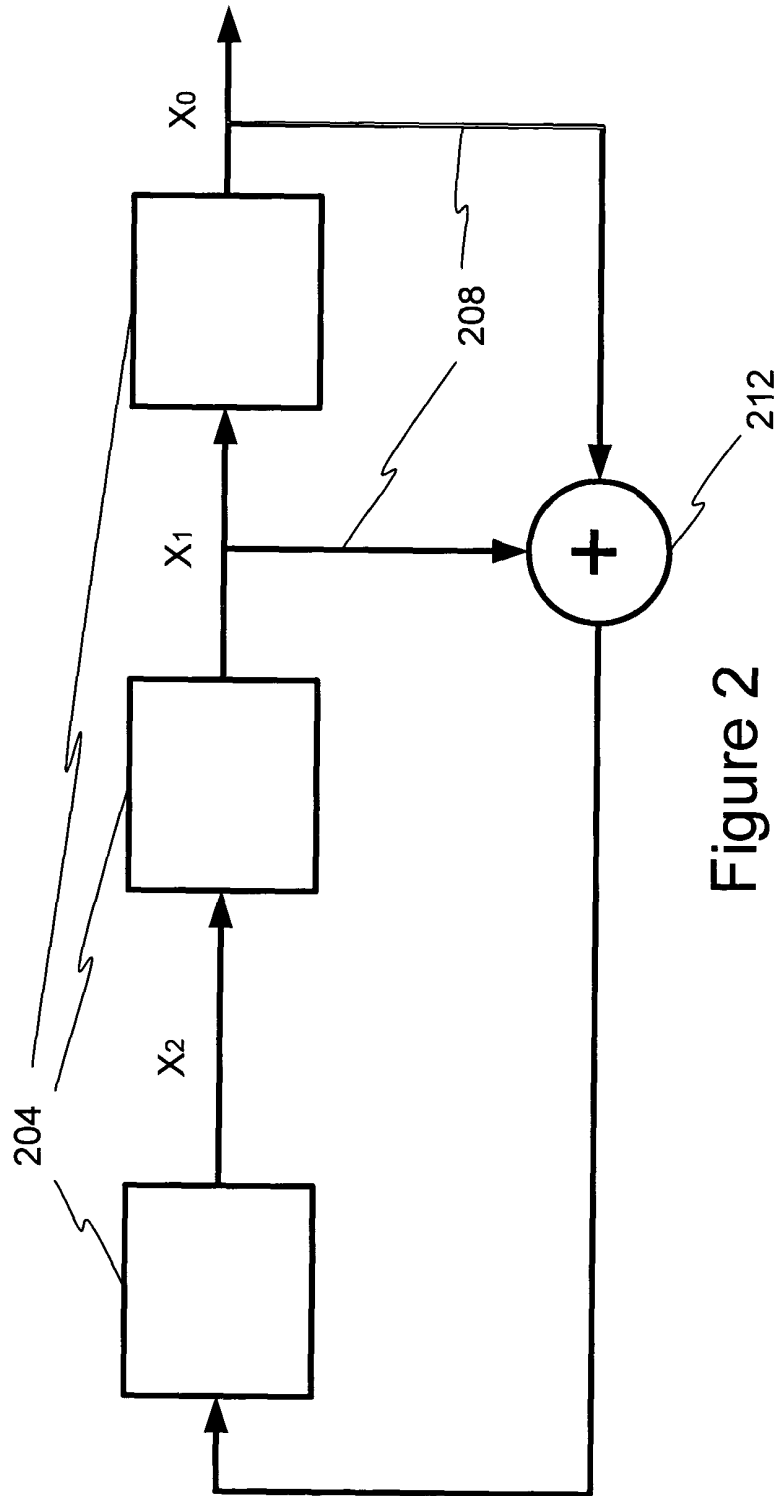


Figure 2

**Output of the same 3-bit LFSR used in
Figure 1 when it is sampled every 3 iterations**

Iteration #	LFSR Outputs ($X_2X_1X_0$)
1	100
2	110
3	001
4	101
5	011
6	010
7	111
8	100

Figure 3

**Output of periodic switching between two 3-bit LFSRs
when the switching is done after every iteration**

ITERATION	LFSR #1	OUTPUT OF PERIODIC SWITCHING	LFSR #2
1	011	011	
2		100	100
3	001	001	
4		110	110
5	100	100	
6		111	111
7	010	010	
8		011	011
9	101	101	
10		101	101
11	110	110	
12		010	010
13	111	111	
14		001	001
15	011	011	
16		100	100

Figure 4

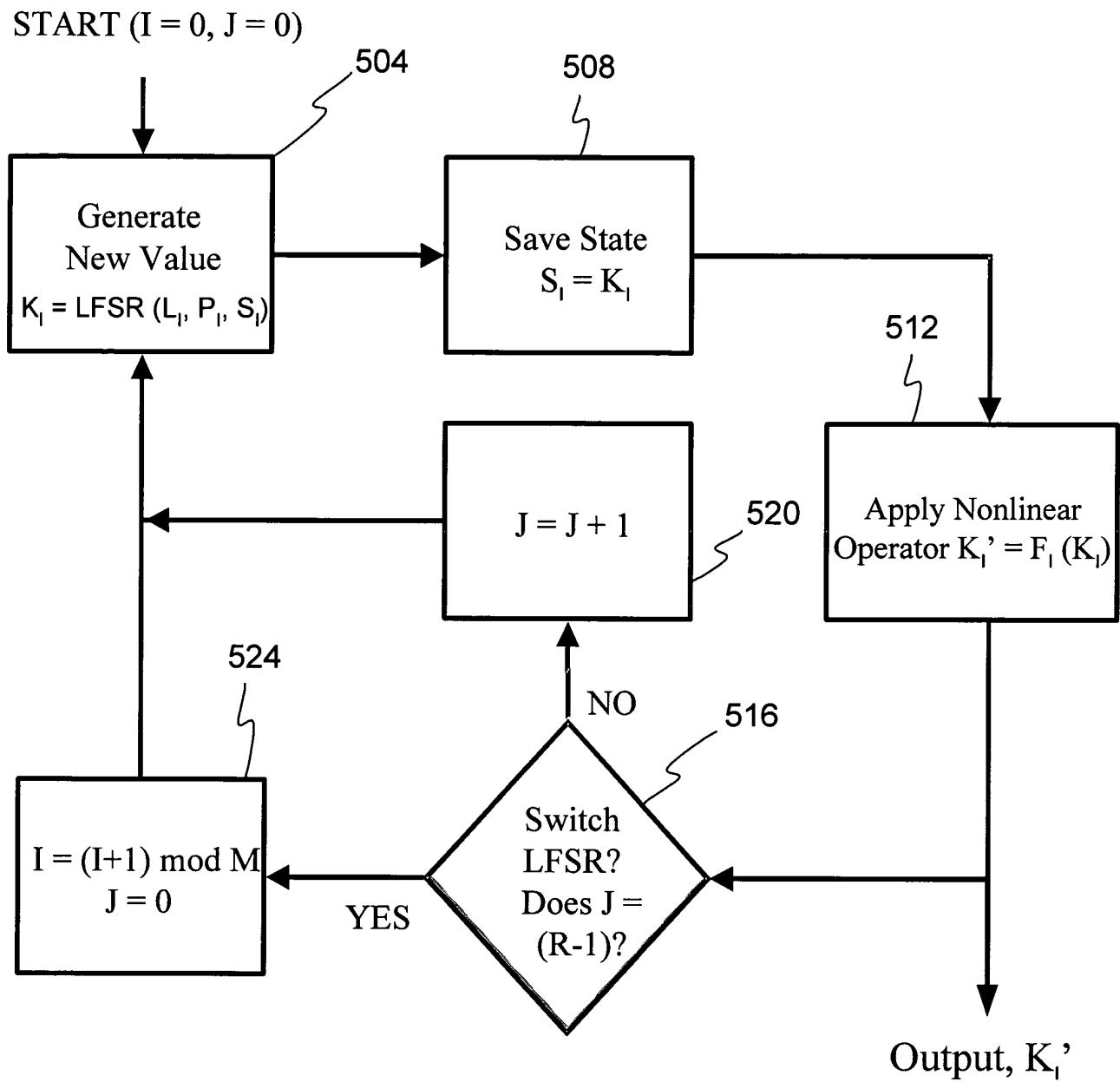


Figure 5

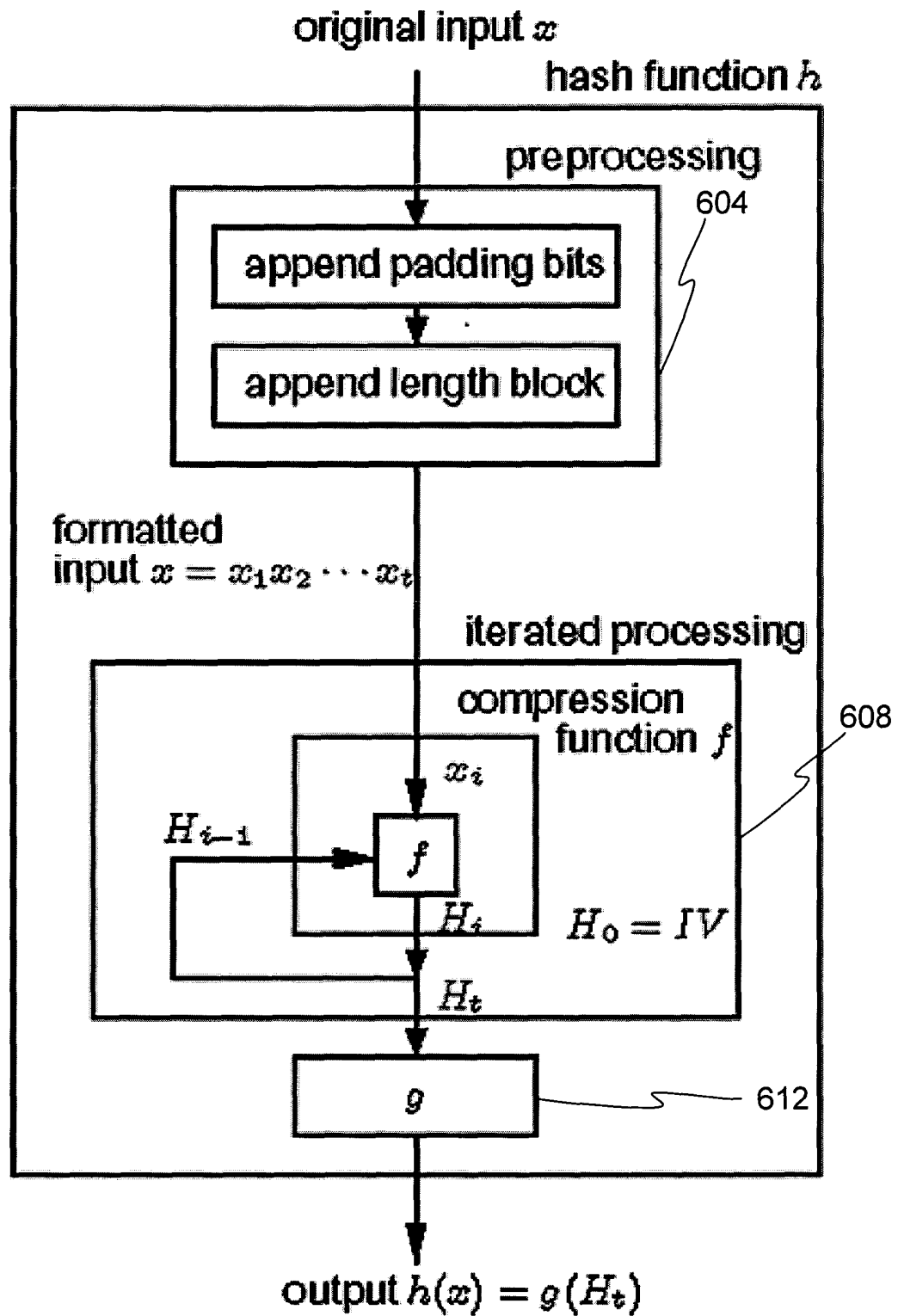


Figure 6